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### PECL Clock Oscillators, SMD Packages

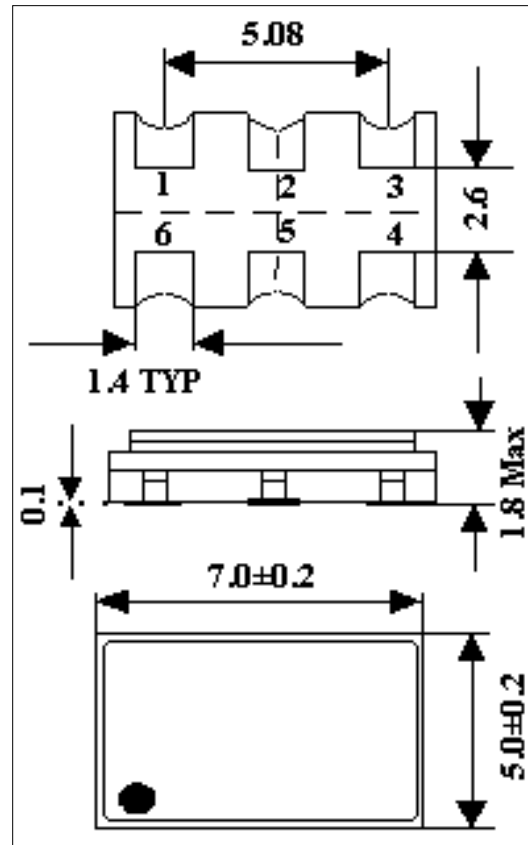
<b>MODEL</b>	<b>C71</b>
<b>FREQ' RANGE</b>	100.00 to 166.00 MHz
<b>FREQ' STABILITY</b>	Standard: $\pm 25$ PPM, $\pm 50$ PPM Parameters included: Operating temp. Range, Calibration at +25°C, Supply & Load Variations, Aging
<b>OPERATING TEMP' RANGE</b>	See "How - To - Order" instructions
<b>OUTPUT WAVEFORM</b>	PECL High Level (VOH): 0.9Vcc Min. Low Level (VOL): 0.1Vcc Max. Symmetry: (50 $\pm$ 10%) or (50 $\pm$ 5%) Rise/ fall time: 5nSec Yyp.
<b>LOAD</b>	15 pF Typ
<b>SUPPLY VOLTAGE</b>	3.3 Vdc , 5.0 Vdc
<b>SUPPLY CURRENT</b>	25 mA Max. for F < 25.00 MHz 35 mA Max. for frequencies between 25.00 MHz to 50.00 MHz 45 mA Max. for F > 50.00 MHz
<b>ENABLE / DISABLE</b>	Enable: High (above 2.2V) or open Disable (High Impedance): Low (below 0.8V)
<b>SMD PACKAGE</b>	7.0 x 5.0 x 1.7 mm. 6 Pads

<b>Environmental Conditions</b>	
<b>SHOCK</b>	IEC 68-2-27 (Test Ea) 30G, 18 mSec, Half Sine
<b>VIBRATION</b>	IEC 68-2-6 (Test Fc), 0.35mm, 5G, 10-2000Hz, 6 cycles/ axis (Fig.2, Table V)
<b>THERMAL SHOCK</b>	IEC 68-2-14 (Test Na), 30 Min. in each extreme temperature
<b>SOLDERING HEAT</b>	235°C $\pm$ 5°C for 10 Sec. Max. Solder melting point: 217°C

## C71

Pin	Function
1	TR-STATE
2	N/C
3	GND
4	Output
5	Output ~
6	Vcc

Dimensions in mm.



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[to top](#)